

FACULTY PROFILE



1. Name: Chandra Prakash Jain

2. Designation: Assistant Professor

3. Qualification: B.E., M.Tech., PhD (Pursuing)

4. Area of Specialization: VLSI Design

5. Number of projects:

i) On-going

Sl. No.	Title of the project	Funding Agency	Amount
1.	Modeling , Simulation and Development of Semiconductor Devices under Radiation Environment in Designing of Radiation Harden Phase Locked Loops (on going)	DST-SERC Fast Track Young Scientist Scheme	17.00 Lac
2.	Radiation Hardened LNA (surrender)	UGC- Major Research Project	6.07 Lac

6. Number of M.Tech candidates Dissertation successfully completed: 27

8. Publications:

i) Books

Sl. No.	Author/s	Title	Publisher	Year of publication
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1	C. P. Jain	Radiation Harden Devices and Circuits for Analog Applications	Lambert Publications, Germany	2011
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ii) Articles in Journals: 04

iii) Papers in Conference/Seminar/Proceedings: 04(International) + 03 (National)

9. Conference/Seminar organized: 07

10. Conference/Seminar/Short term course participated: 20

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