

GOVERNMENT ENGINEERING COLLEGE AJMER
 DEPARTMENT OF COMPUTER ENGINEERING & INFORMATION TECHNOLOGY
 PRACTICAL EXAMINATION SCHEDULE (REVISED) FOR B. TECH. VII SEM (CS & IT)

REF. NO.: ECA/CS & IT/2017/ *845*

Date: 1/12/2017

DATE/ DAY	BRANCH	GROUP	TIME	PRACTICAL	LAB
6/12/2017 WED	CS	C1	8:15 AM-2:15 PM	PROJECT DEMO-3	COMPUTER CENTER
		C2			
		C3			
		C4			
	IT	I1	8:15 AM-2:15 PM	PROJECT DEMO-3	
		I2			
7/12/2017 THU	CS	C1	8:15 AM-11:15 AM	Discipline & Extra Curricular Activities	A
		C2	11:15 AM-2:15 PM		
		C2	8:15 AM-11:15 AM	Compiler Design Lab	G-18 B
		C1	11:15 AM-2:15 PM		
		C3	8:15 AM-11:15 AM	Practical Training*	D
		C4	11:15 AM-2:15 PM		
	C4	8:15 AM-11:15 AM	VLSI Physical Design Lab	G-13	
	C3	11:15 AM-2:15 PM			
	IT	I1	8:15 AM-11:15 AM	Computer Graphics & Multimedia Lab	B
		I2	11:15 AM-2:15 PM		
8/12/2017 FRI	CS	C1	8:15 AM-11:15 AM	Web Development Lab	A
		C2	11:15 AM-2:15 PM		
		C3	8:15 AM-11:15 AM	Discipline & Extra Curricular Activities	G-29
		C4	11:15 AM-2:15 PM		
		C4	8:15 AM-11:15 AM	Compiler Design Lab	G-18 B
		C3	11:15 AM-2:15 PM		
	IT	I1	8:15 AM-11:15 AM	Practical Training*	G-13
		I2	11:15 AM-2:15 PM		
		I2	8:15 AM-11:15 AM	Internet Programming Lab	C
		I1	11:15 AM-2:15 PM		
11/12/2017 MON	CS	C1	8:15 AM-11:15 AM	Practical Training*	A
		C2	11:15 AM-2:15 PM		
		C2	8:15 AM-11:15 AM	VLSI Physical Design Lab	G-13
		C1	11:15 AM-2:15 PM		
		C3	8:15 AM-11:15 AM	Web Development Lab	B
		C4	11:15 AM-2:15 PM		
	IT	I2	8:15 AM-11:15 AM	Discipline & Extra Curricular Activities	C
		I1	11:15 AM-2:15 PM		
		I1	8:15 AM-11:15 AM	DMW Lab	G-18 B
		I2	11:15 AM-2:15 PM		

GROUP	FROM	TO
C1	14EEACS001	14EEACS025
C2	14EEACS026	14EEACS051
C3	14EEACS052	14EEACS077
C4	14EEACS078	LAST + BACK
I1	14EEAIT001	14EEAIT035
I2	14EEAIT037	LAST + BACK

Dilip
DILIP SODIA
 FINAL YEAR (CS & IT), EXAM COORDINATOR

Rakesh
DR. RAKESH RATHI
 HEAD (CS & IT)

COPY 1) PA TO PRINCIPAL
 TO: 2) EXAM COORDINATOR
 3) FACULTY CIRCULATION

4) NOTICE BOARD
 5) DEPARTMENT FILE
 6) UG COORDINATOR