

GOVT. ENGINEERING COLLEGE AJMER

Department of Electronics & Communication Engineering

I / II-Mid Term Mercy Test* and I-Back Improvement Test**, 2018-19

Ref: GECA/ECE/2019/1635


Date: 19/03/2019


Date	B. Tech VI Sem.	Session
30.03.2019	6EC1A MICROWAVE ENGINEERING-II	S-I
30.03.2019	6EC2A MICROPROCESSORS	S-II
01.04.2019	6EC3A INDUSTRIAL ELECTRONICS	S-I
01.04.2019	6EC4A DIGITAL COMMUNICATION	S-II
02.04.2019	6EC5A CONTROL SYSTEMS	S-I
02.04.2019	6EC6.3A OPTICAL FIBER COMMUNICATION	S-II
	<i>B. Tech VIII Sem.</i>	
03.04.2019	8EC1A- IC TECHNOLOGY	S-I
03.04.2019	8EC2A RADAR & TV ENGINEERING	S-II
04.04.2019	8EC3A MEMS & NANOTECHNOLOGY	S-I
04.04.2019	8EC4.1A COMPUTER NETWORKS	S-II

Time Schedule: S-I: 11:30 - 12:30 AM S-II: 1:00 - 2:00 PM

*The students appearing in the mercy test, are required take prior permission from HOD. The suitable venue and time (if mentioned session time is not convenient) of the exam may be decided by the concerned subject teacher. Only one mercy test (I /II) is permitted. If required, the practical batch/session may be changed with prior permission of HOD.

**The I-Back Improvement Test is common to the I-mercy test as per above schedule. The II-Back Imp. Test is common to the II-Midterm Test to be held before this mercy exam as per schedule already displayed.


(Dept. Int. Exam Coordinator-UG)


(Head, ECE)

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- Concerned Dept Heads
- Concerned faculty members by circulation (Duty Chart attached),
- Guard File

Main Coordinator (Exams.)
Deptt. Notice Boards,